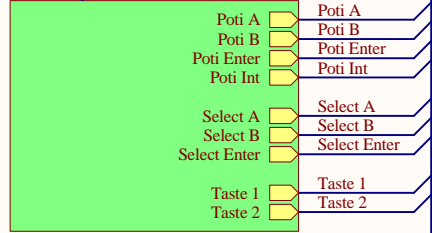
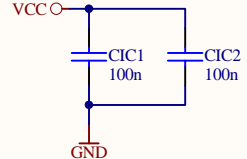
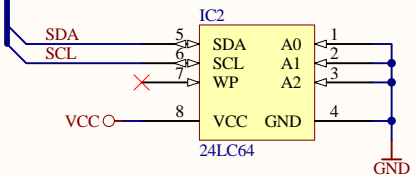
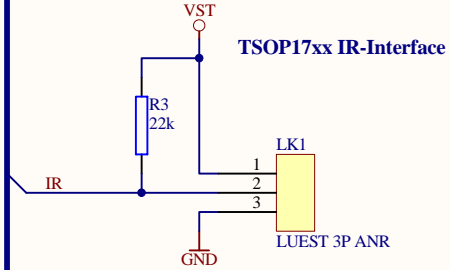
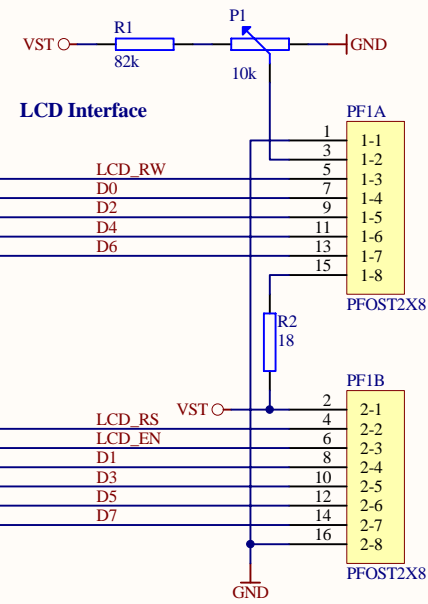
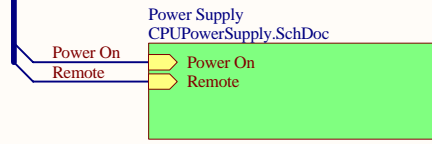
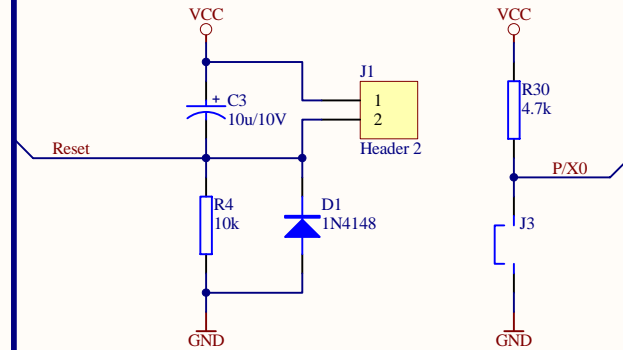
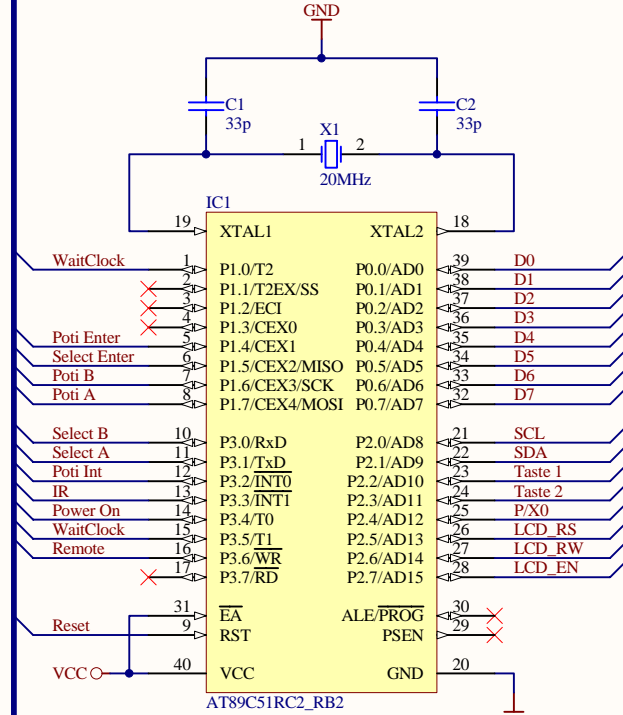
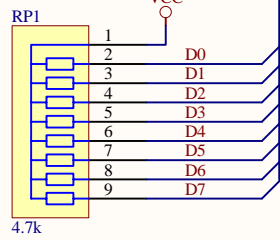
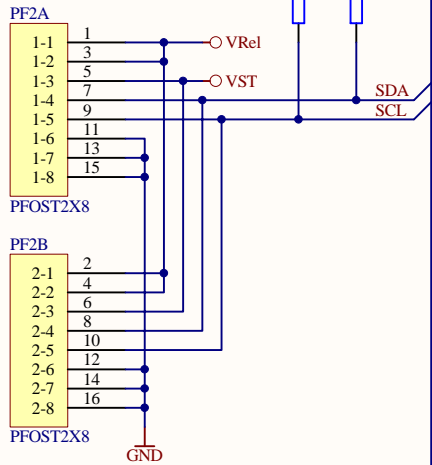


Encoder Input
EncoderInput.SchDoc



I2C-Bus Erweiterung



| | | |
|------------------------------|-------------------------------|-----------------------|
| Title | | |
| VV4 - Control Circuit | | |
| Size | Number | Revision |
| A4 | | 1 |
| Date: | 01.06.2007 | Sheet of |
| File: | D:\User\...\ControlCpu.SchDoc | Drawn By: Ralph Stens |